

NPN SILICON RF TWIN TRANSISTOR

μ PA862TS

NPN SILICON RF TRANSISTOR (WITH 2 DIFFERENT ELEMENTS) IN A 6-PIN SUPER LEAD-LESS MINIMOLD

FEATURES

- · Low voltage operation
- 2 different built-in transistors (2SC5435, 2SC5800)

Q1: Built-in high gain transistor

 $f_T = 12.0 \text{ GHz TYP.}$, $|S_{21e}|^2 = 11.0 \text{ dB TYP.}$ @ $V_{CE} = 3 \text{ V}$, $I_C = 10 \text{ mA}$, f = 2 GHz

Q2: Built-in low phase distortion transistor suited for OSC applications

 $f_T = 4.5 \text{ GHz TYP.}, |S_{21e}|^2 = 4.0 \text{ dB TYP.} @ Vce = 1 \text{ V, Ic} = 5 \text{ mA, f} = 2 \text{ GHz}$

· 6-pin super lead-less minimold package

BUILT-IN TRANSISTORS

	Q1	Q2
Flat-lead 3-pin thin-type ultra super minimold part No.	2SC5435	2SC5800

ORDERING INFORMATION

Part Number	Quantity	Supplying Form
μPA862TS	50 pcs (Non reel)	8 mm wide embossed taping
μPA862TS-T3	10 kpcs/reel	Pin 1 (Q1 Collector), Pin 6 (Q1 Base) face the perforation side of the tape

Remark To order evaluation samples, contact your nearby sales office.

The unit sample quantity is 50 pcs.

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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ABSOLUTE MAXIMUM RATINGS ($T_A = +25$ °C)

Parameter	Symbol	Ratings		Unit
		Q1	Q2	
Collector to Base Voltage	Vсво	9	9	V
Collector to Emitter Voltage	Vceo	6	5.5	V
Emitter to Base Voltage	VEBO	2	1.5	V
Collector Current	lc	30	100	mA
Total Power Dissipation	Ptot Note	110 in 1 element		mW
		130 in 2 elements		
Junction Temperature	Tj	150		°C
Storage Temperature	T _{stg}	-65 to +150		°C

 $\textbf{Note} \hspace{0.3cm} \text{Mounted on 1.08 cm}^2 \times 1.0 \hspace{0.1cm} \text{mm (t) glass epoxy PCB}$

ELECTRICAL CHARACTERISTICS (TA = +25°C)

(1) Q1

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Collector Cut-off Current	Ісво	VcB = 5 V, IE = 0 mA	_	-	100	nA
Emitter Cut-off Current	ІЕВО	V _{EB} = 1 V, I _C = 0 mA	1	ı	100	nA
DC Current Gain	hfE Note 1	VcE = 3 V, Ic = 10 mA	75	110	150	-
Gain Bandwidth Product	f⊤	VcE = 3 V, Ic = 10 mA, f = 2 GHz	10.0	12.0	-	GHz
Insertion Power Gain	S _{21e} ²	VcE = 3 V, Ic = 10 mA, f = 2 GHz	7.0	11.0	-	dB
Noise Figure	NF	$V_{CE} = 3 \text{ V}, \text{ Ic} = 3 \text{ mA}, \text{ f} = 2 \text{ GHz}, $ $Z_S = Z_{opt}$	_	1.5	2.5	dB
Reverse Transfer Capacitance	Cre Note 2	VcB = 3 V, IE = 0 mA, f = 1 MHz	_	0.4	0.7	pF

(2) Q2

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Collector Cut-off Current	Ісво	VcB = 5 V, IE = 0 mA	-	-	600	nA
Emitter Cut-off Current	ІЕВО	VEB = 1 V, Ic = 0 mA	-	-	600	nA
DC Current Gain	hfE Note 1	VcE = 1 V, Ic = 5 mA	100	120	145	1
Gain Bandwidth Product (1)	f⊤	VcE = 1 V, Ic = 5 mA, f = 2 GHz	3.0	4.5	1	GHz
Gain Bandwidth Product (2)	f⊤	VcE = 1 V, Ic = 15 mA, f = 2 GHz	5.0	6.5	1	GHz
Insertion Power Gain (1)	S _{21e} ²	VcE = 1 V, Ic = 5 mA, f = 2 GHz	3.0	4.0	1	dB
Insertion Power Gain (2)	S _{21e} ²	VcE = 1 V, Ic = 15 mA, f = 2 GHz	4.5	5.5	1	dB
Noise Figure	NF	$V_{CE} = 1 \text{ V}, \text{ Ic} = 10 \text{ mA}, \text{ f} = 2 \text{ GHz},$ $Z_{S} = Z_{opt}$	_	1.9	2.5	dB
Reverse Transfer Capacitance	Cre Note 2	Vcb = 0.5 V, IE = 0 mA, f = 1 MHz	_	0.6	0.8	pF

Notes 1. Pulse measurement: PW \leq 350 μ s, Duty Cycle \leq 2%

2. Collector to base capacitance when the emitter grounded

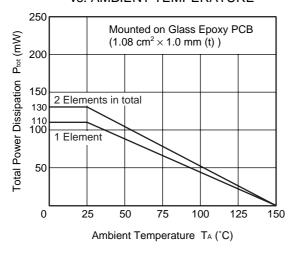
hfe CLASSIFICATION

Rank	FB
Marking	vY
hre Value of Q1	75 to 150
hre Value of Q2	100 to 145

3

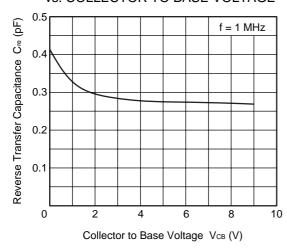
★ TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



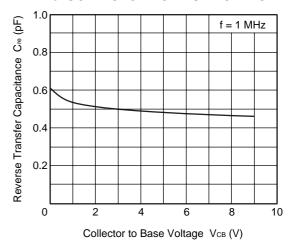
Q1

REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE



Q2

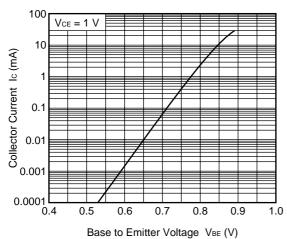
REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE



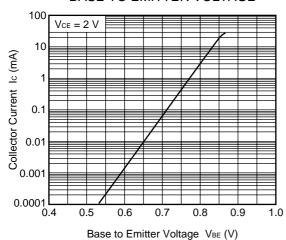
Remark The graphs indicate nominal characteristics.

Q1

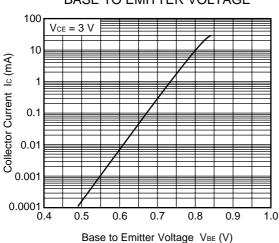
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



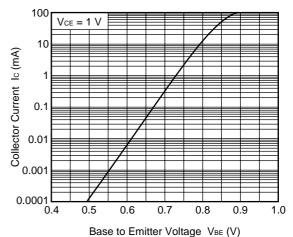
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



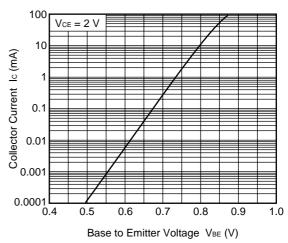
Remark The graphs indicate nominal characteristics.

Q2

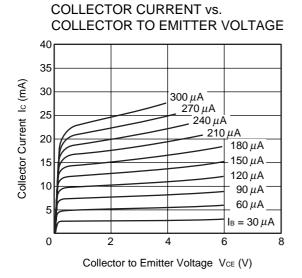
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE

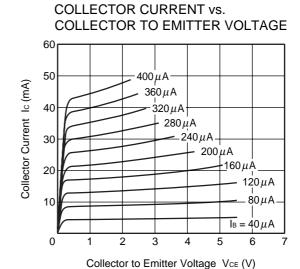


Q1

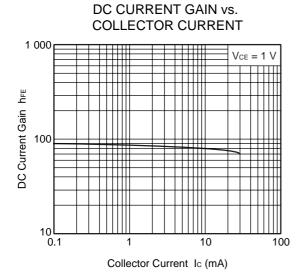


Remark The graphs indicate nominal characteristics.

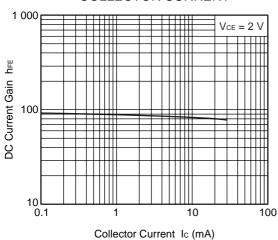
Q2



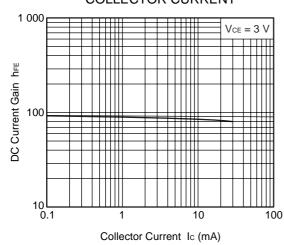
Q1



DC CURRENT GAIN vs. COLLECTOR CURRENT

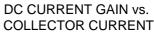


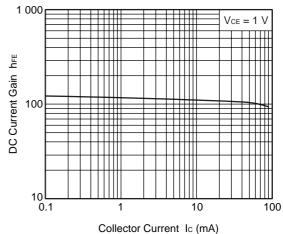
DC CURRENT GAIN vs. COLLECTOR CURRENT



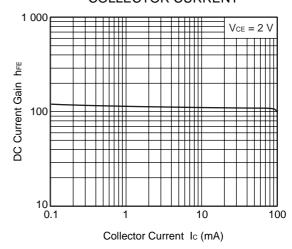
Remark The graphs indicate nominal characteristics.

Q2





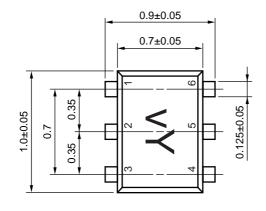
DC CURRENT GAIN vs. COLLECTOR CURRENT

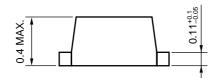


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PACKAGE DIMENSIONS

6-PIN SUPER LEAD-LESS MINIMOLD (UNIT: mm)





PIN CONNECTIONS

- 1. Collector (Q1)
- 2. Emitter (Q1)
- 3. Collector (Q2)
- 4. Base (Q2)
- 5. Emitter (Q2)
- 6. Base (Q1)

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