## RT3NHHM

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

### DESCRIPTION

RT3NHHM is compound transistor built with two RT1N436 chips in SC-88 package.

#### **FEATURE**

Silicon NPN epitaxial type.

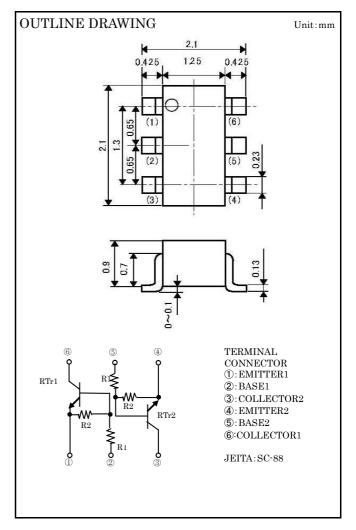
Built in bias resistor.

Each transistor elements are independent.

Mini package for easy mounting

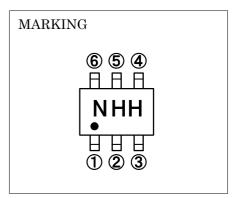
### APPLICATION

Inverted circuit, switching circuit, interface circuit, driver circuit



# MAXIMUM RATING (Ta=25°C) (RTr1, RTr2.)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
VIN	Input voltage	30	V
Ic	Collector current	100	mA
Icm	Peak Collector current	200	mA
Pc	Collector dissipation(Total, Ta=25°C)	150	mW
Tj	Junction temperature	+150	°C
$T_{ m stg}$	Storage temperature	-55 <b>~</b> +150	ပ



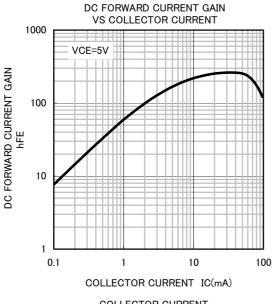
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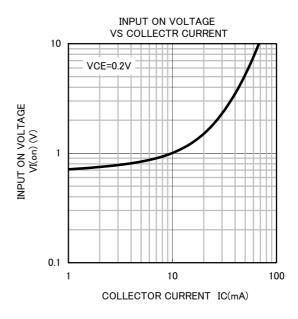
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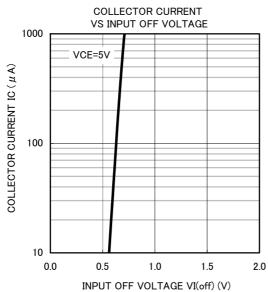
# ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr1, RTr2.)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	Onit
V(BR)CEO	Collector to Emitter break down voltage	I c=100μA, R <sub>BE</sub> =∞	50	-	_	V
ICBO	Collector cut off current	V <sub>CB</sub> =50V, I <sub>E</sub> =0	-	-	0.1	μА
hfe	DC forward current gain	V <sub>CE</sub> =5V, I C=10mA	80	-	_	-
VCE(sat)	Collector to Emitter saturation voltage	I c=10mA, I <sub>B</sub> =0.5mA	-	-	0.3	V
VI(ON)	Input on voltage	V <sub>CE</sub> =0.2V, I <sub>C</sub> =5mA	ı	0.8	1.4	V
VI(OFF)	Input off voltage	$V_{CE}$ =5V, I $_{C}$ =100 $\mu A$	0.4	0.6	_	V
R <sub>1</sub>	Input resistor		3.3	4.7	6.1	ΚΩ
R <sub>2</sub> /R <sub>1</sub>	Resistor ratio		8	10	12	-
fT	Gain band width product	$V_{CE}$ =6V, I $_{E}$ =-10mA	_	200	_	MHz

# TYPICAL CHARACTERISTICS (RTr1, RTr2)









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